

Nicholas APPLETON

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(remove the Z's, they are an anti-spam measure)

Academic History

2005 – Current	<u>University of Technology, Sydney</u> Bachelor of Engineering (Electrical), Diploma in Engineering Practice
1998 – 2003	<u>Blaxland High School</u> Completed Higher School Certificate 2003 UAI of 74.65

Achievements

2010	UTS Dean's list
2009	UTS Dean's list
2002	Achieved 7 th position in the state in the 2002 UNSW CISRA Programming Competition.

Work History

March 2010 to
present

University of Technology Sydney, Sydney
Casual Academic

Tutoring in the following subjects:

- Introduction to Electrical Engineering – teaching fundamentals of electrical engineering: Kirchhoff's Voltage/current laws, differential equations, frequency domain response, mesh and nodal circuit analysis, phasor analysis, transfer functions.
- Signals and Systems – assisting students in laboratory sessions with topics such as FFTs, block diagrams, phase locked loops, modulation and demodulation.
- Electronics and Circuits – assisting students in laboratory sessions with general circuit experiments.
- Embedded Software – assisting students with development in the C programming language for embedded systems on an Motorola MC9S12 based platform.

June 2007 to
February 2008

Peter D.G. Jewkes Pty. Ltd., Ermington
Trainee Organ Builder

Peter D.G. Jewkes is the largest pipe organ building, tuning and restoration firm in NSW. This job was taken during a semester deferment from my degree.

Tasks included:

- Maintained organs throughout NSW including major restoration work on a historically significant instrument in Mudgee.
- Assisted in general office duties.

June 2006 to
February 2007

Star Electrical, Condell Park
Cadet Engineer

Star Electrical provides electrical services on commercial construction projects. The job was taken as part of the industrial placement required in the UTS engineering degree.

Duties included:

- Working with subcontractors, inducting apprentices onto site and corresponding with the builder. This developed my communication skills in making demands, explaining procedures clearly and writing formally.
- Checking electrical drawings conformed to the electrical standards imposed on a construction site.
- Keeping their IT system up to date. (Keeping all important documents scanned and filed properly on the system).
- Performing electrical tender counts for jobs.
- Numerous document preparation using Microsoft Word and Excel.

Skills

Technical Skills

- Assembling printed circuit boards (SMT and through-hole).
- Fault finding and repairing old analogue electronic organs (pre 1980s organs specifically).
- Proficient in designing and writing assembly applications for digital systems using PIC microcontrollers (see website for some projects which I have published <http://www.appletonaudio.com/>)

Computer Skills

- Very experienced with object oriented Pascal (Delphi) programming – many projects written during high school (mainly sound producing, MIDI interfacing or small graphical applications).
- Experienced with structural C and object oriented C++ programming. Several of my projects are released on my website (<http://www.appletonaudio.com/>).
- Experienced in the use of the Microsoft Office suite – proficient in the use of Word, Excel and Access.
- Competent in the use of HTML and CSS in the production of my own website.
- Proficient at assembling, repairing and upgrading computer system hardware.

Personal Skills

- In my previous job with Star Electrical, I developed strong teamwork skills while working on a high-profile and large scale job.
- I am outgoing and like meeting new people.
- I am an effective and life-long learner and enjoy challenges.
- I have a professional phone manner which I developed throughout my time at Star Electrical.

Licenses

- WorkCover NSW Occupational Health and Safety General Induction (Green Card)

Interests

- Playing classical pipe organ.
- Designing and building electronic MIDI interfaces based on PIC microcontrollers.
- Writing audio related DSP software, specifically for digital audio sampling software which I publish on my website

Referees

Referees will be provided on request.

Appendix: UTS Subjects Current/Completed

Bachelor of Engineering (Electrical), Diploma in Engineering Practice

<i>Stage</i>	<i>Subject</i>	<i>Result</i>
Autumn 2010	Object Oriented Design	Current
	Engineering Enterprise	Current
	Capstone A	Current
Spring 2009	Engineering Project Management	67 Credit
	Embedded Software	96 High Distinction
	Analogue Electronics	91 High Distinction
Autumn 2009	Engineering Economics Finance	83 Distinction
	Real-time Operating Systems	87 High Distinction
	Advanced Digital Systems	86 High Distinction
	Digital Electronics	86 High Distinction
Spring 2008	Design Fundamentals	76 Distinction
	Data Acquisition Distribution	91 High Distinction
	Advanced Mathematics Physics	69 Credit
Autumn 2008	Electromechanical Systems	87 High Distinction
	Signals and Systems	73 Credit
Autumn 2007	Software Engineering	75 Distinction
	Introductory Digital Systems	79 Distinction
	Signal Theory	83 Distinction
Autumn 2006	Engineering Communication	84 Distinction
	Embedded C	98 High Distinction
	Circuit Analysis	53 Pass
Spring 2005	Object Oriented Programming	95 High Distinction
	Electronics and Circuits	87 High Distinction
	Mathematical Modelling 2	83 Distinction
	Introduction to Mechanical and Mechatronic Engineering	83 Distinction
Autumn 2005	Intro to Electrical Engineering	93 High Distinction
	Engineering for Sustainability	71 Credit
	Physical Modelling	69 Credit
	Mathematical Modelling 1	59 Pass